




# Lecture (00) Road Map

By:  
**Dr. Ahmed ElShafee**

Dr. Ahmed ElShafee, ACU : Spring 2018, CSE303 Logic design II

## About the course

Course name, Code	CSE303 Logic design II
Pre-requisite	CSE302
Credit hours	3
Number Sessions	8-12
Date, time, place	
Text book	<i>Charles H. Roth, Larry L. Kinney "Fundamentals of Logic Design Edition" 8th ed</i>

Email	Ahmed.elshafee@acu.edu.eg
Presentations and materials	Aelshafee.net

# Objectives

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- To analyze and design combinational and sequential logic circuits.

# TOC

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- Multi-Level Gate Circuits NAND and NOR Gates
- Combinational Circuit Design and Simulation Using Gates
- Multiplexers, Decoders, and Programmable Logic Devices
- Introduction to VHDL
- Latches and Flip-Flops
- Registers and Counters
- Analysis of Clocked Sequential Circuits
- Derivation of State Graphs and Tables
- Reduction of State Tables
- Sequential Circuit Design
- VHDL for Sequential Logic

# Grading Scheme

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item	Grade
final	40
midterm	20
Quizzes	10
Attendance and participation	10
Laboratories	10
deliverables	10

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Thanks,..

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