



كلية الهندسة

Faculty of Engineering



جامعة الأهرام الكندية
AHRAM CANADIAN UNIVERSITY

Model Answer

Course name: Microprocessors

Exam number: Midterm Exam – spring 2017

Course Code: ECE303

Exam Date: March /2017

Lecturer: Dr. Ahmed ElShafee

Time Allowed: 60 minutes

ID:

Name:

Q1	Q2	Q3	Q4	Q5	Q6	Total
3	3	3	3	4	4	20

Q1	Complete the following statement, put your answer in the following table	
	The 80286 addresses _____ bytes of memory.	<div style="border: 1px solid black; padding: 5px; text-align: center;">16M bytes</div>
	How much memory is found in the DOS transient program area?	<div style="border: 1px solid black; padding: 5px; text-align: center;">640K</div>
	The 8086 microprocessor addresses _____ bytes of memory.	<div style="border: 1px solid black; padding: 5px; text-align: center;">1M</div>
	the first microprocessors that address 4G bytes of memory?	<div style="border: 1px solid black; padding: 5px; text-align: center;">80386</div>
	The personal computer system addresses _____ bytes of I/O space.	<div style="border: 1px solid black; padding: 5px; text-align: center;">64K</div>
	The extended BX register is addressed as _____.	<div style="border: 1px solid black; padding: 5px; text-align: center;">EBX</div>

Q3	Are these instructions legal or illegal, if not explain why		
	Instruction	Legal/illegal	Why
	MOV BX,DS	<div style="border: 1px solid black; padding: 5px; width: fit-content; margin: 0 auto;">legal</div>	<p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>
	MOV CL,AX	<div style="border: 1px solid black; padding: 5px; width: fit-content; margin: 0 auto;">illegal</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content; margin: 0 auto;">illegal; because using different size register ,source 16-bit & destination 8-bit</div> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>
	Mov 0x43,AL	<div style="border: 1px solid black; padding: 5px; width: fit-content; margin: 0 auto;">illegal</div>	<div style="border: 1px solid black; padding: 5px; width: fit-content; margin: 0 auto;">illegal; because using destination can't be constant</div> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>

Q4	<p>If AX=1001H and DX=20FFH, list the sum and the contents of each flag register bit (C, A, S, Z, and O) after the ADD AX, DX instruction executes.</p>
	<div style="border: 1px solid black; padding: 10px; margin: 10px auto; width: 80%;"> <p>1001H + 20FFH = 3100H C = 1, A = 0, S = 0, Z = 0, and O = 0</p> </div>

Q5	What are the values of AX in each instruction in the following program	
	instruction	AX value
	MOV AX,00A1H	<p>.....</p> <p>AX=00A1H</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>
	MOV AH,01H	<p>.....</p> <p>AX=01A1H</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>
	ADD AX,01B3H	<p>.....</p> <p>AX=01A1H + 01B3H = 0354H</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>

Q6 Consider the following code in debug program

```
- a 1500
1234:1500 db 35,96,42,0
```

1. Show in a table the contents of memory location 1498 to 1505
2. Write code starts at effective address 100, that add the first 3 bytes in previous instruction, en replace 0 of the 4th byte with the addition result.

Lowe bank		Higer bank	
Address	contents	address	content
1498	x	1499	x
1500	35	1501	96
1502	42	1503	0
1504	x	1505	x

```
.....
..... -a 100 .....
..... mov [1500],35 .....
..... mov [1501],96 .....
..... mov [1502],42 .....
..... mov [1503],0 .....
..... Mov bx,1500 .....
..... Add al,[bx] .....
..... Add al,[bx+1] .....
..... Add al,[bx+2] .....
..... Mov [bx+3],al .....
.....
.....
.....
.....
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```



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