



Lecture (00) Road Map

By:

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Dr. Ahmed ElShafee, ACU : Spring 2017, Logic Design I

TOC

- Introduction
- Number Systems and Conversion
- Boolean Algebra
- Applications of Boolean Algebra
- Minterm and Maxterm Expansions
- Karnaugh Maps
- Quine-McCluskey Method
- Multi-Level Gate Circuits
- NAND and NOR Gates
- Combinational Circuit Design and Simulation Using Gates
- Multiplexers, Decoders, and Programmable
- Logic Devices
- Introduction to VHDL

Grading Scheme

| item | Grade |
|------------------------------|-------|
| final | 40 |
| midterm | 20 |
| Attendance and participation | 10 |
| Laboratories | 10 |
| Quizzes | 10 |
| Deliverables | 10 |



**Thanks,..
Lets do this,**