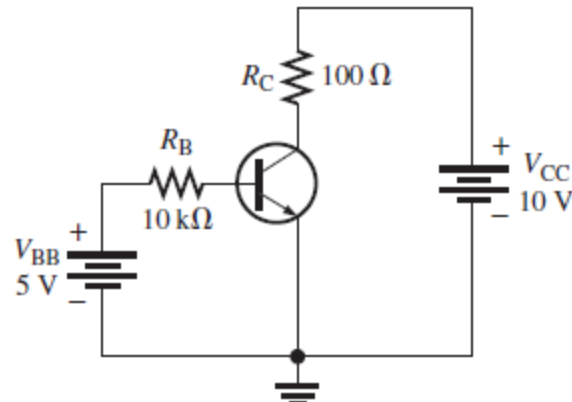
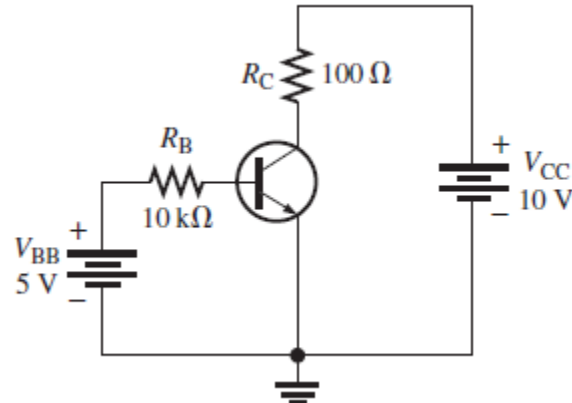
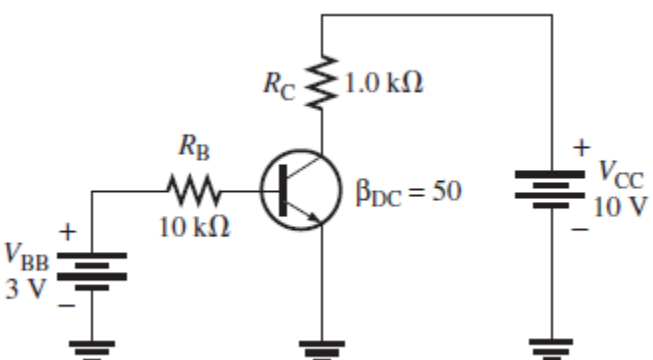


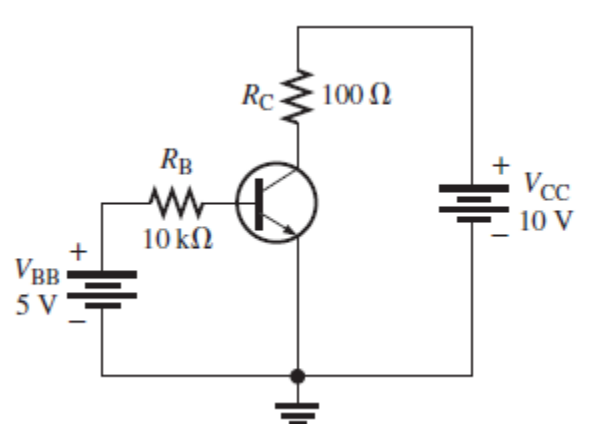
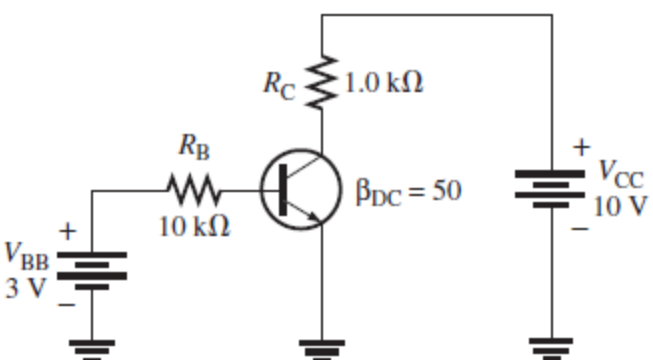
# Electronic Circuits - Tutorial 07

## BJT transistor 1

#	Question	
1	A bipolar junction transistor has three terminals.	T
2	For operation in the linear or active region, the base-emitter junction of a transistor is forward biased.	T
3	The base current and collector current are approximately equal.	F
4	DC and hFE are two different transistor parameters.	F
5	Amplification is the output voltage divided by the input current.	F
6	The three regions of a BJT are base, emitter, and cathode.	F
7	Two types of BJT are npn and pnp.	T
8	The dc voltage gain of a transistor is designated bDC.	F

MCQ

#	Question	
1	 <p>If a transistor with a higher <math>\beta_{DC}</math> is used in Figure, the collector current will  <b>(a) increase (b) decrease (c) not change</b></p>	a
2	 <p>If a transistor with a higher <math>\beta_{DC}</math> is used in Figure, the base current will  <b>(a) increase (b) decrease (c) not change</b></p>	c
3	 <p>If <math>V_{CC}</math> in Figure is increased, the base current will  <b>(a) increase (b) decrease (c) not change</b></p>	c
4	<p>The three terminals of a bipolar junction transistor are called  <b>(a) p, n, p (b) n, p, n (c) input, output, ground (d) base, emitter, collector</b></p>	d
5	<p>For operation as an amplifier, the base of an <i>npn</i> transistor must be  <b>(a) connected to ground (b) connected to V_BB (c) connected to V_CC (d) connected to the collector</b></p>	a

	(a) positive with respect to the emitter (b) negative with respect to the emitter (c) positive with respect to the collector (d) 0 V	
6	The $\beta_{DC}$ of a transistor is its (a) current gain (b) voltage gain (c) power gain (d) internal resistance	a
7	The approximate voltage across the forward-biased base-emitter junction of a silicon BJT is (a) 0 V (b) 0.7 V (c) 0.3 V (d) $V_{BB}$	b
8	If the output of a transistor amplifier is 5 V rms and the input is 100 mV rms, the voltage gain is (a) 5 (b) 500 (c) 50 (d) 100	c
9	 <p>If a transistor with a higher <math>\beta_{DC}</math> is used in Figure, the emitter current will (a) increase (b) decrease (c) not change</p>	a
10	 <p>If <math>V_{BB}</math> is reduced in Figure, the collector current will (a) increase (b) decrease (c) not change</p>	b
11	In a <i>pn</i> p transistor, the <i>p</i> regions are (a) base and emitter (b) base and collector (c) emitter and collector	c
12	The emitter current is always (a) greater than the base current (b) less than the collector current (c) greater than the collector current (d) answers (a) and (c)	d
13	If <i>k</i> is 50 times larger than <i>h</i> , then is $\beta_{DC}$ (a) 0.02 (b) 100 (c) 50 (d) 500	c
14	The bias condition for a transistor to be used as a linear amplifier is called (a) forward-reverse (b) forward-forward (c) reverse-reverse (d) collector bias	a
15	When a lowercase is used in relation to a transistor, it refers to (a) a low resistance (b) a wire resistance (c) an internal ac resistance (d) a source resistance	c













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Q5	For normal operation of a <i>pnp</i> transistor, the base must be (+ or -) with respect to the emitter, and (+ or -) with respect to the collector.
Sol 5	..... Negative, positive .....





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Q7	What is the $\alpha_{DC}$ when $I_C = 8.23 \text{ mA}$ and $I_E = 8.69 \text{ mA}$ ?
Sol 7	..... 0.947 .....







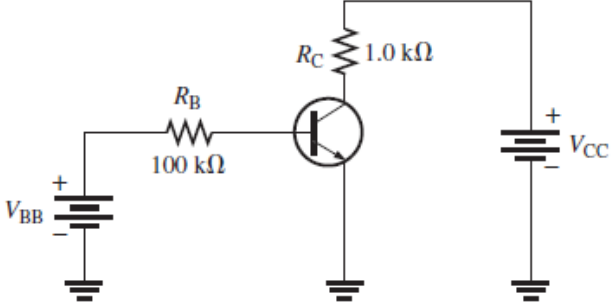
Q1 1	A certain transistor exhibits an $\alpha_{DC}$ of 0.96. Determine $I_C$ when $I_E = 9.35$ mA.
Sol 11	..... ... 8.98 mA .....



Q1  
2

A base current of  $50\ \mu\text{A}$  is applied to the transistor in Figure 4–53, and a voltage of 5 V is dropped across  $R_C$ . Determine the  $\beta_{DC}$  of the transistor.

FIGURE 4–53



Sol  
12

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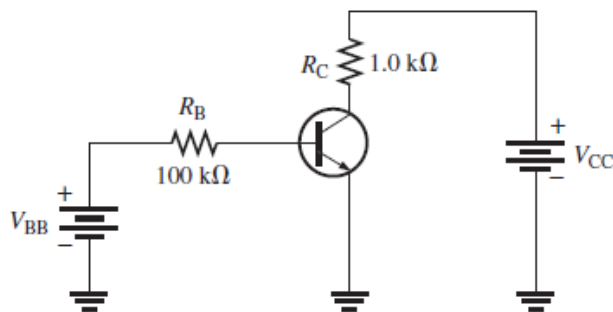
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Q1  
3

Calculate  $\alpha_{DC}$  for the transistor in

A base current of  $50 \mu\text{A}$  is applied to the transistor in Figure 4–53, and a voltage of 5 V is dropped across  $R_C$ . Determine the  $\beta_{DC}$  of the transistor.

FIGURE 4-53



Sol  
13

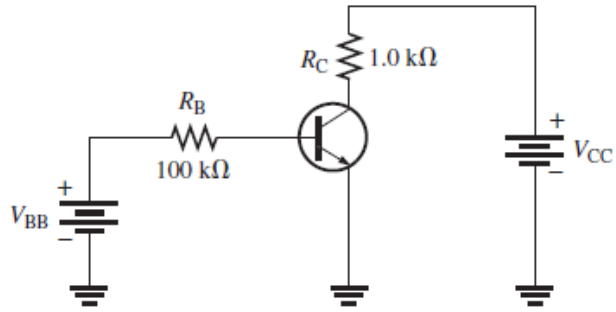
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Q1  
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Assume that the transistor in the circuit of Figure 4–53 is replaced with one having a  $\beta_{dc}$  of 200. Determine  $I_B$ ,  $I_C$ ,  $I_E$ , and  $V_{CE}$  given that  $V_{CC} = 10 \text{ V}$  and  $V_{BB} = 3 \text{ V}$ .

FIGURE 4–53



Sol  
14

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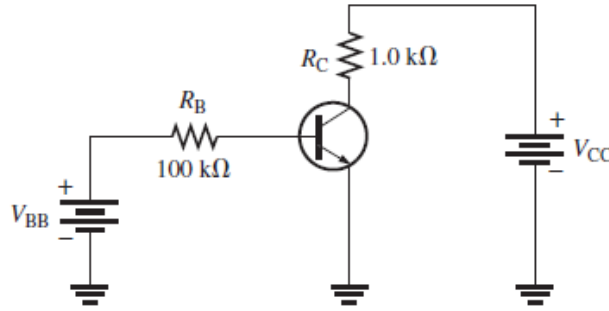
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Q1  
5

If  $V_{CC}$  is increased to 15 V in Figure 4-53, how much do the currents and  $V_{CE}$  change?

FIGURE 4-53

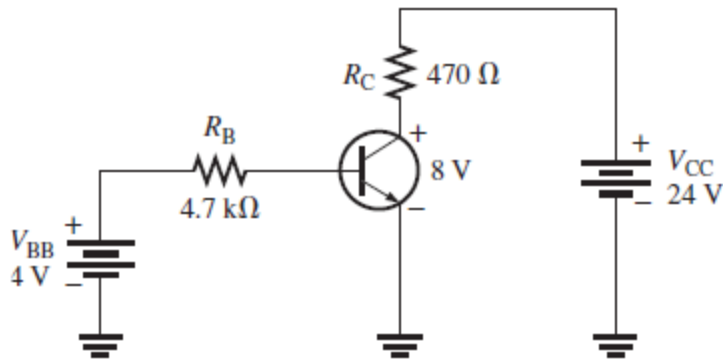


Sol  
15

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... 5.3 V increase .....  
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Q1  
6

Determine each current in Figure 4-54. What is the  $\beta_{DC}$ ?



Sol  
16

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