

Electronic Circuits – Assignment

02

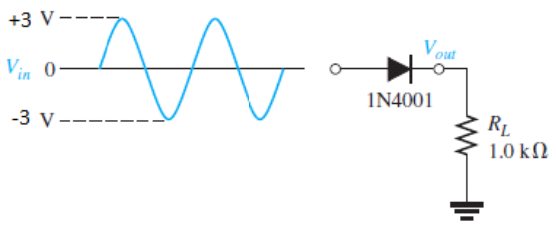
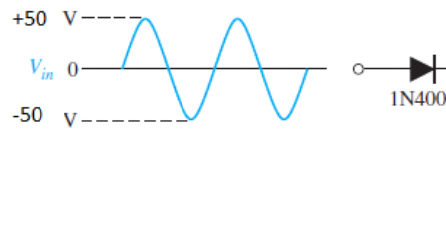
Diode Applications I

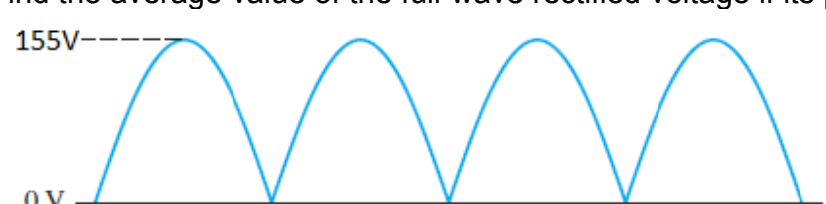
#	Student ID	Student Name	Grade (10)
-			

Delivery Date	
---------------	--

1. يتم تسليم التمرين محلولا في خلال أسبوع من تاريخ التمرين، و يتم حذف درجتين من التمرين عن كل أسبوع تأخير
2. يتم التسليم لمعيد المقرر مباشرة
3. تتم أجابه التمرين في نفس ورق الأسئلة

Problems 1

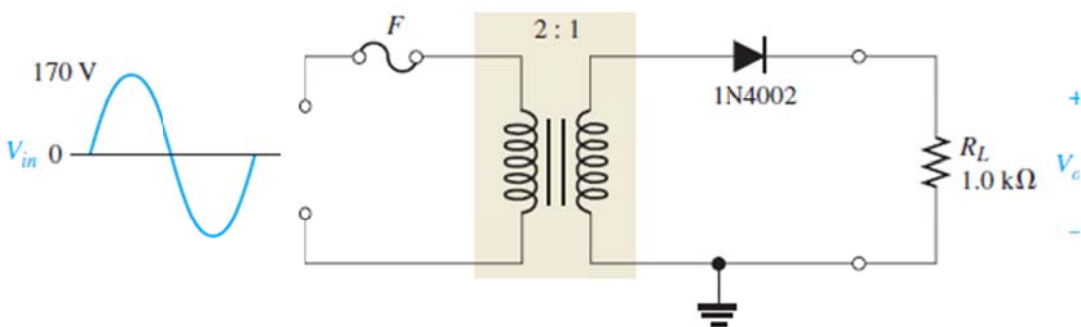
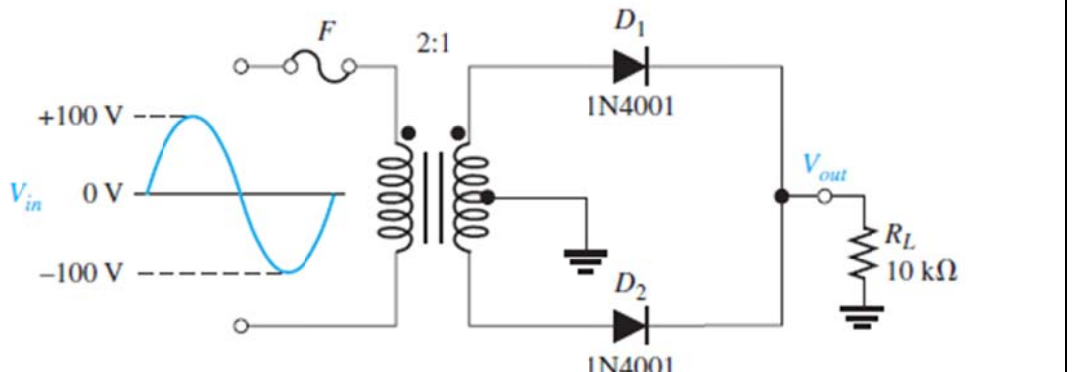
<p>Problem 1</p>	<p>Determine the peak output voltages for the rectifiers in Figure 2-24 if the peak input in part (a) is 3 V and the peak input in part (b) is 50 V.</p> <div style="display: flex; justify-content: space-around; align-items: flex-start;"> <div style="text-align: center;">  <p>(a)</p> </div> <div style="text-align: center;">  <p>(b)</p> </div> </div>
<p>Answer 1</p>	<p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>

<p>Problem 2</p>	<p>Find the average value of the full-wave rectified voltage if its peak is 155 V.</p> 
<p>Answer 2</p>	<p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>

T & False questions

#	Question	Answer
1	The two regions of a diode are the anode and the collector.	
2	A diode conducts current when forward-biased.	
3	Two types of current in a diode are electron and hole.	
4	The output frequency of a half-wave rectifier is twice the input frequency.	
5	PIV stands for positive inverse voltage.	
6	The output frequency of a full-wave rectifier is twice the input frequency.	

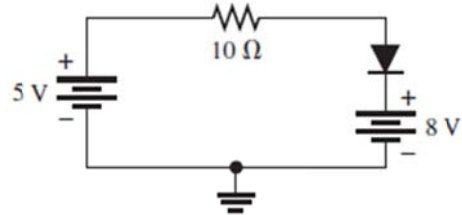
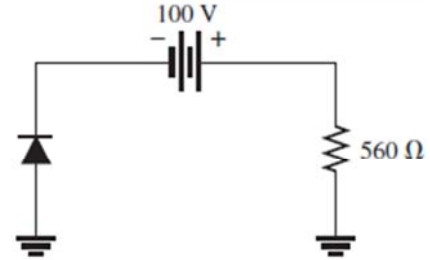
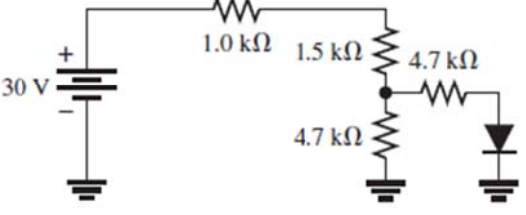
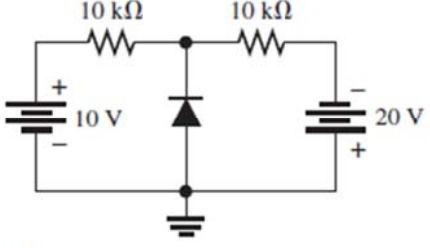
MCQ 1

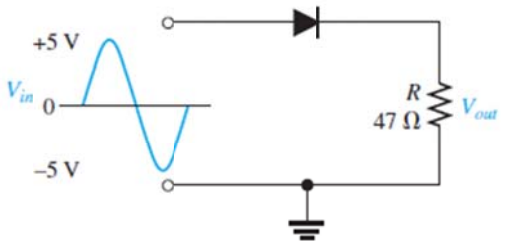
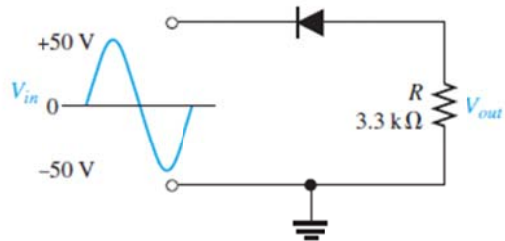
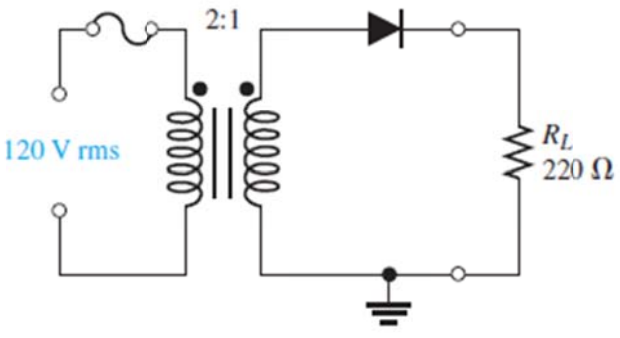
#	Question
1	When a diode is forward-biased and the bias voltage is increased, the voltage across the diode (assuming the practical model) will (a) increase (b) decrease (c) not change
2	When a diode is reverse-biased and the bias voltage is increased, the reverse current (assuming the complete model) will (a) increase (b) decrease (c) not change
3	If the forward current in a diode is increased, the diode voltage (assuming the practical model) will (a) increase (b) decrease (c) not change
4	If the barrier potential of a diode is exceeded, the forward current will (a) increase (b) decrease (c) not change
5	If the turns ratio of the transformer in Figure 2–28 is decreased, the forward current through the diode will  (a) increase (b) decrease (c) not change
6	If the PIV rating of the diodes in Figure 2–36 is increased, the current through R_L will  (a) increase (b) decrease (c) not change

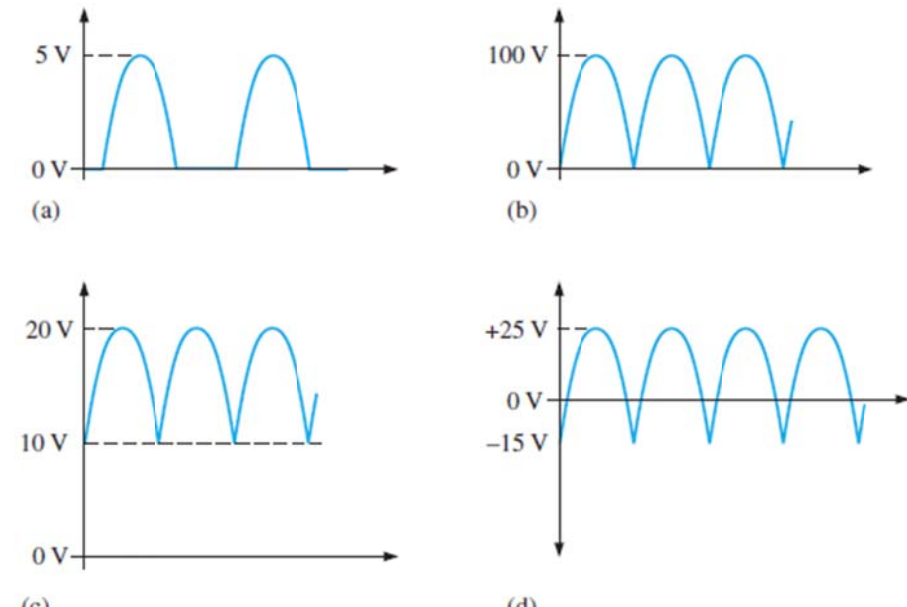
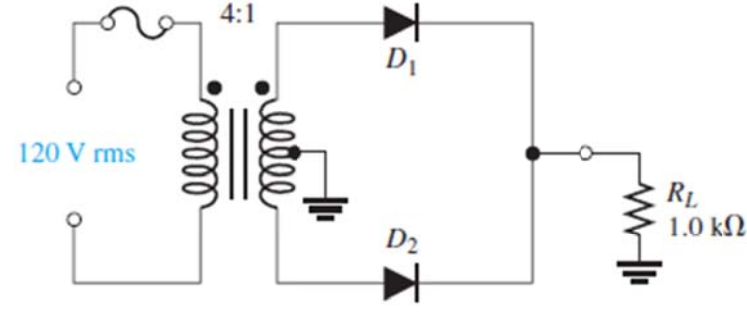
MCQ 2

#	Question	
1	To forward-bias a diode, (a) an external voltage is applied that is positive at the anode and negative at the cathode (b) an external voltage is applied that is negative at the anode and positive at the cathode (c) an external voltage is applied that is positive at the p region and negative at the n region (d) answers (a) and (c)	
2	Although current is blocked in reverse bias, (a) there is some current due to majority carriers (b) there is a very small current due to minority carriers (c) there is an avalanche current	
3	When forward-biased, a diode (a) blocks current (b) conducts current (c) has a high resistance (d) drops a large voltage	
4	The dynamic resistance can be important when a diode is (a) reverse-biased (b) forward-biased (c) in reverse breakdown (d) unbiased	
5	Ideally, a diode can be represented by a (a) voltage source (b) resistance (c) switch (d) all of these	
6	In the complete diode model, (a) the barrier potential is taken into account (b) the forward dynamic resistance is taken into account (c) the reverse resistance is taken into account (d) all of these	
7	When a 60 Hz sinusoidal voltage is applied to the input of a half-wave rectifier, the output frequency is (a) 120 Hz (b) 30 Hz (c) 60 Hz (d) 0 Hz	
8	For the circuit in Question 15, the diode must be able to withstand a reverse voltage of (a) 10 V (b) 5 V (c) 20 V (d) 3.18 V	
9	When a 60 Hz sinusoidal voltage is applied to the input of a full-wave rectifier, the output frequency is (a) 120 Hz (b) 60 Hz (c) 240 Hz (d) 0 Hz	
10	When the peak output voltage is 100 V, the PIV for each diode in a center-tapped full-wave rectifier is (neglecting the diode drop) (a) 100 V (b) 200 V (c) 141 V (d) 50 V	

Problems 2

Q2	Explain why a series resistor is necessary when a diode is forward-biased.
Sol 2	<p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>
Q4	What would cause the barrier potential of a silicon diode to decrease from 0.7 V to 0.6 V?
Sol 4	<p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>
<div style="display: flex; justify-content: space-around;"> <div style="text-align: center;">  <p>(a)</p> </div> <div style="text-align: center;">  <p>(b)</p> </div> </div> <div style="display: flex; justify-content: space-around; margin-top: 20px;"> <div style="text-align: center;">  <p>(c)</p> </div> <div style="text-align: center;">  <p>(d)</p> </div> </div>	
Q6	Determine the voltage across each diode in Figure 2-92, assuming the practical model.
Sol 6	<p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>

Q8	Determine the voltage across each diode in Figure 2–92, using the complete diode model with $r_f = 10 \Omega$ and $r_r = 100 \text{ M}\Omega$.
Sol 8	<p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>
(a)	
(b)	
Q10	What is the peak inverse voltage across each diode in Figure 2–93?
Sol 10	<p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>
Q12	What is the peak forward current through each diode in Figure 2–93?
Sol 12	<p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>
	

Q14	Determine the peak and average power delivered to R_L in Figure 2–94.
Sol 14	<p>.....</p> <p>.....</p> <p>.....</p> <p>.....</p>
	
	
Q16	<p>Consider the circuit in Figure 2–96.</p> <p>(a) What type of circuit is this?</p> <p>(b) What is the total peak secondary voltage?</p> <p>(c) Find the peak voltage across each half of the secondary.</p> <p>(d) Sketch the voltage waveform across R_L.</p> <p>(e) What is the peak current through each diode?</p> <p>(f) What is the PIV for each diode?</p>

